Paper / Subject Code: 38915 / Microprocessor

S.E. SEM IV / C SCHEME / COMP / AIDS / MAY 2023 / 19.05.2023

[Max Marks:80] **Duration: 3hrs** N.B.: (1) Question No 1 is Compulsory. (2) Attempt any three questions out of the remaining five. (3) All questions carry equal marks. (4) Assume suitable data, if required and state it clearly. [20] Attempt any FOUR a Explain the following instructions: STOSB, DAA related to 8086. Discuss in brief the protection mechanism of 80386DX Explain the maximum mode of 8086 Explain in brief cache organization of Pentium processor Write an assembly language program for 8086 to exchange contents of two memory blocks [10] Draw the timing diagrams for Read and Write operations in minimum and maximum mode [10] Explain hyper threading technology and its use in Pentium 4 [10] Interface DMA controller 8257 with 8086 MP. Explain different data transfer modes of 8257 DMAC [10] Write an ALP for 8086 to reverse a string of 10 characters. [10] Compare 80386, Pentium 1, Pentium 2 and Pentium 3 Processor. [10] Explain MESI protocol [10] Explain the Register organization of 80386. b Explain the Initialization command words (ICWs) and Operational command words(OCWs) of the 8259 PIC. [10] Design 8086 microprocessor-based on following Specifications: 1. MP 8086 working at 10MHz minimum mode. 32 KB ROM using 8 KB Devices 16 KB RAM using 4KB chips [10] Explain 8255 with a block diagram and its operating modes